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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of

Shone Fuja

Group Art Unit: 2818

Application No. 10/750,893

Examiner: David Vu

Filed: January 5, 2004

For: NON-VOLATILE MEMORY ARRAY HAVING VERTICAL TRANSISTORS
AND MANUFACTURING METHOD THEREOF

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

U.S. Patent and Trademark Office
Customer Service Window, Mail Stop Amendment
Randolph Building
401 Dulany Street
Alexandria VA 22314

Date: June 16, 2005

Sir:

In response to the Office Action of May 17, 2005, Applicant elects the invention of Group I, Embodiment I, Claims 18-30, drawn to a non-volatile memory array having vertical transistors and a gate dielectric layer comprising at least one nitride film. The election is without traverse.

Respectfully submitted,

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